Specifcation of products

:

:MDS75A-H1

ZHEJIANG SHILING SEMICONDUCTOR CO.,LTD.

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ZHEJIANG SHILING SEMICONDUCTOR CO.,

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T_j(°C)	VALUE			UNIT
				Min	Type	Max	
I_O	DC output current	Three-phase full wave rectifying circuit, T_C=100°C	150				A
V_{RRM}	Repetitive peak reverse voltage	V_{RRM} tp=10ms V_{RSM}= V_{DRM}&V_{RRM}+200V	150	600			V
I_{RRM}	Repetitive peak current	at V_{RRM}	150				
I_{FSM}	Surge for ward current	10ms half sine wave€H …€ ? @ Â<	150	ËwAyW1Ž↑		0.75	KA
I^2t	I^2T for fusing coordination	V_R=0.6V_{RRM}					A^2s*10^3
V_{TO}	Threshold voltage					0.80	V

RR=0.6V

CUTLINE DRAWING & CIRCUIT DIAGRAM

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Package Outline Information

